Milestone #3: Static Single-Assignment

Overview
For this milestone, you will augment your compiler to use virtual registers for all local variables and parameters. This will require modifications to the LLVM instruction generation to ensure that all virtual register use is in static single-assignment (SSA) form.

It is recommended that you follow the algorithm of Braun, et. al., to construct LLVM in SSA form with virtual registers as discussed in lecture and described in the paper linked from the course website.

Command-line Options
The output of your compiler should default to the LLVM representation with local variables in virtual registers. You will want to maintain the option to generate the LLVM representation with stack allocation (from milestone #2) to allow comparisons between the two representations at the end of the course. Your compiler should support command-line option(s) to dictate which representation is emitted.